

ABSTRACT OF THE DISCLOSURE



A high speed active matrix-type liquid crystal display device which can perform accurate gradation display for each one field (frame), by eliminating fluctuations in pixel voltage which accompany changes in capacitance of a liquid crystal. The device includes a pixel electrode and a MOS transistor circuit which drives the pixel electrode. The MOS transistor circuit is disposed in the vicinity of a cross-over point of a scanning line and a signal line, and includes a first MOS transistor in which a gate electrode is connected to the scanning line, and one of a source electrode and a drain electrode is connected to the signal line. The MOS transistor circuit also includes [[an]] a source follower type analog amplifier in which an input electrode is connected to the other one of the source electrode and the drain electrode of the first MOS transistor, [[a]] one of a plurality of power supply ~~electrode~~ electrodes is connected to the scanning line, and an output electrode is connected to the pixel electrode.